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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10075464	FILING DATE 02/15/2002	CLASS H38 257	SUBCLASS 296	GALL 2811	EXAMINER QINTC
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\*\*APPLICANTS: Kawanaka Shigeru;

\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-381458 12/14/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		219713US2
Verified and Acknowledged Examiners's initials		
TITLE : Semiconductor memory device and its manufacturing method		

U.S. DEPT. OF COMM /PAT. & TM-PTO-435L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs.Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Applicati n Examiner	
PREPARED FOR ISSUE			
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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